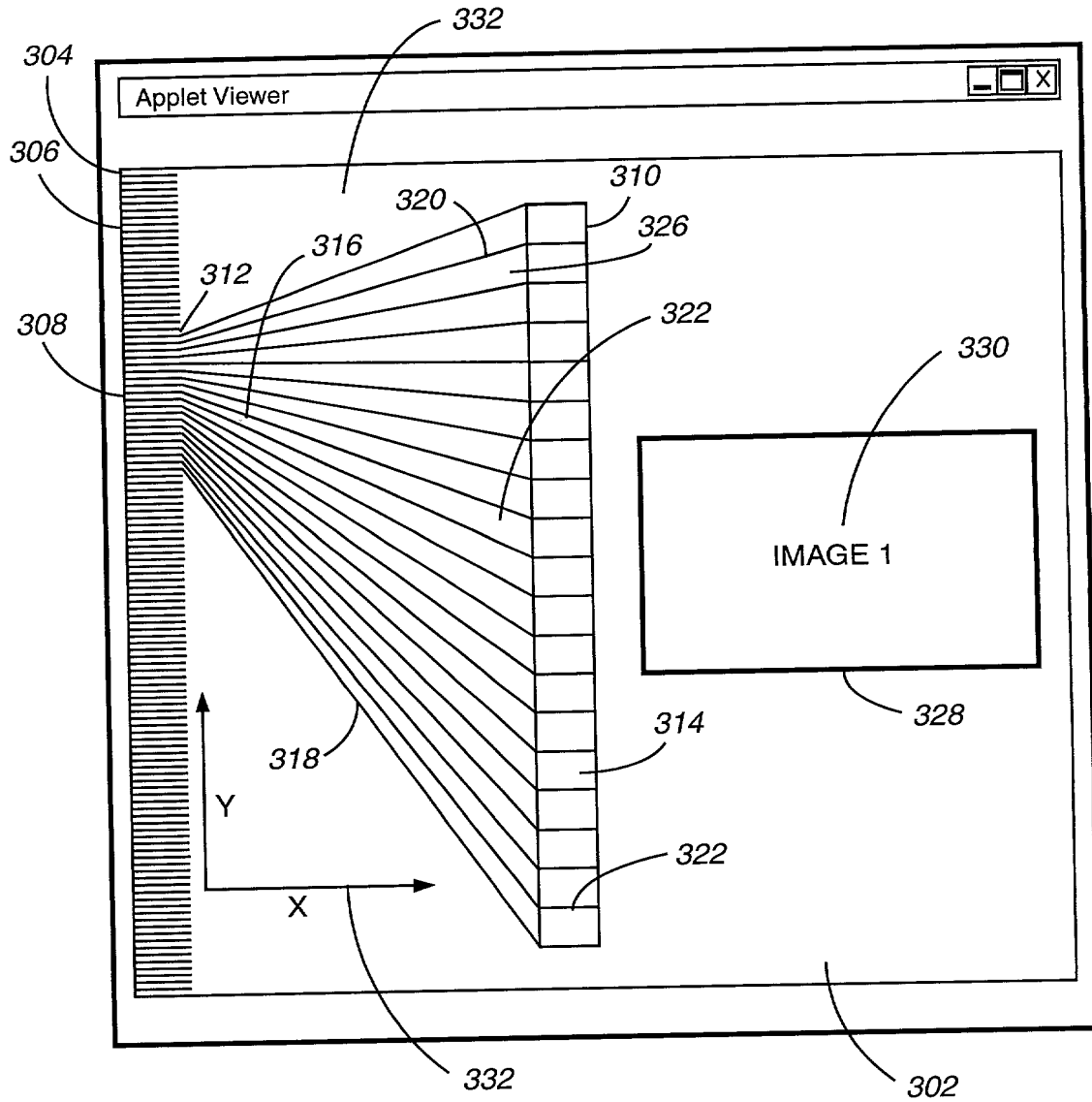


FIG. 1

FIG. 2

FIG. 1 is a block diagram of a computer system architecture. FIG. 2 is a diagram of a software stack.



300

FIG. 3

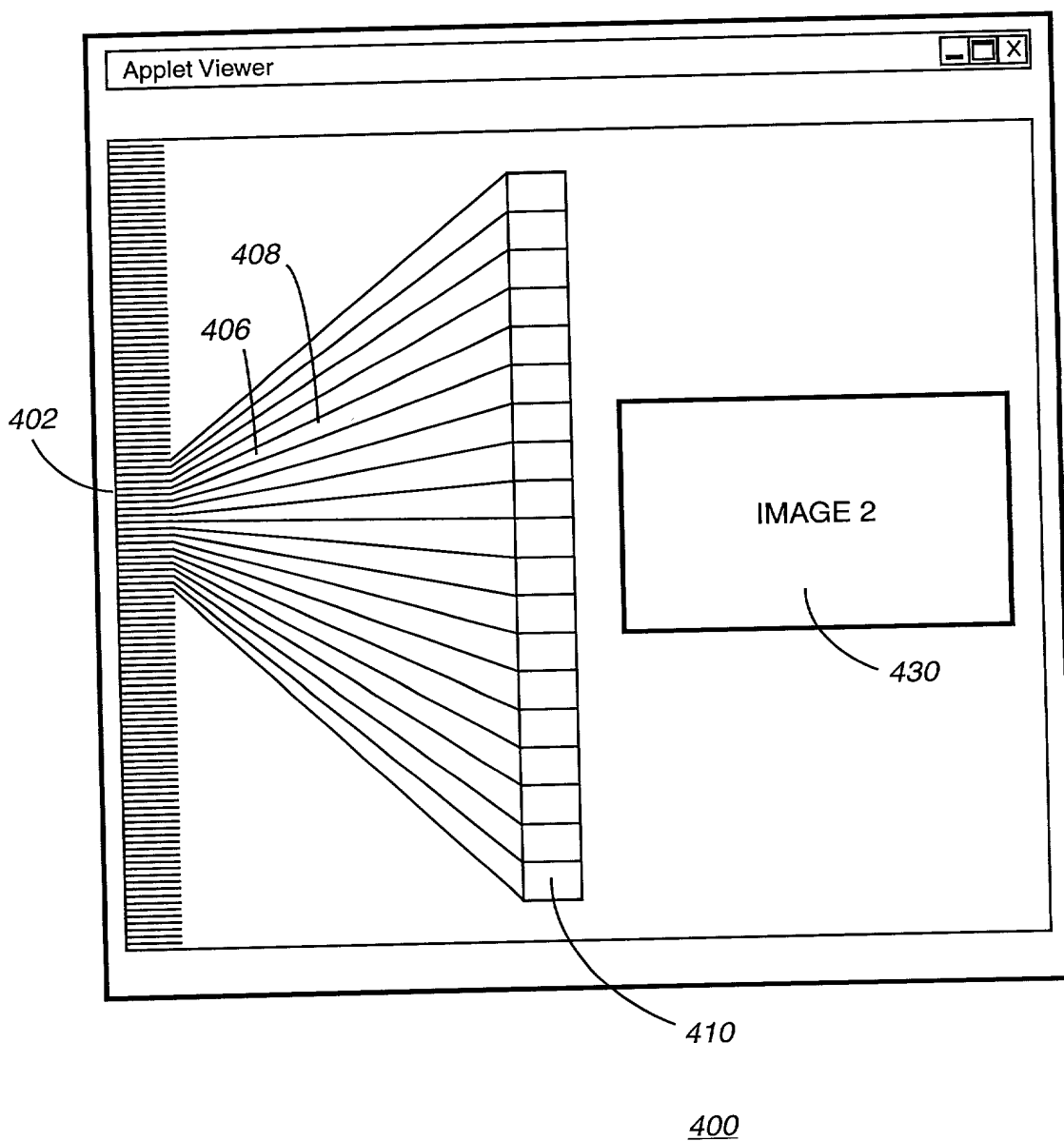
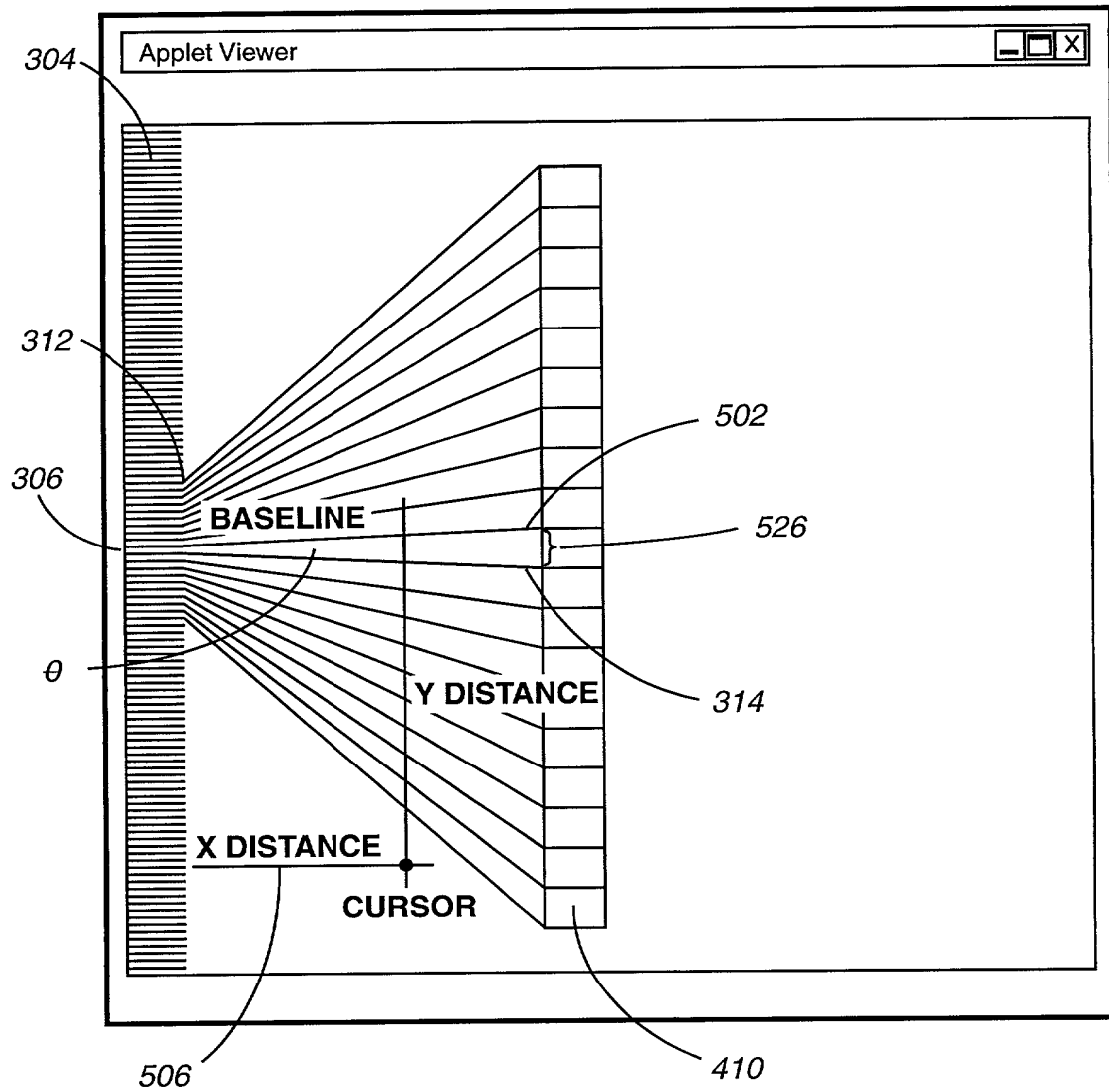


FIG. 4



500**FIG. 5A**

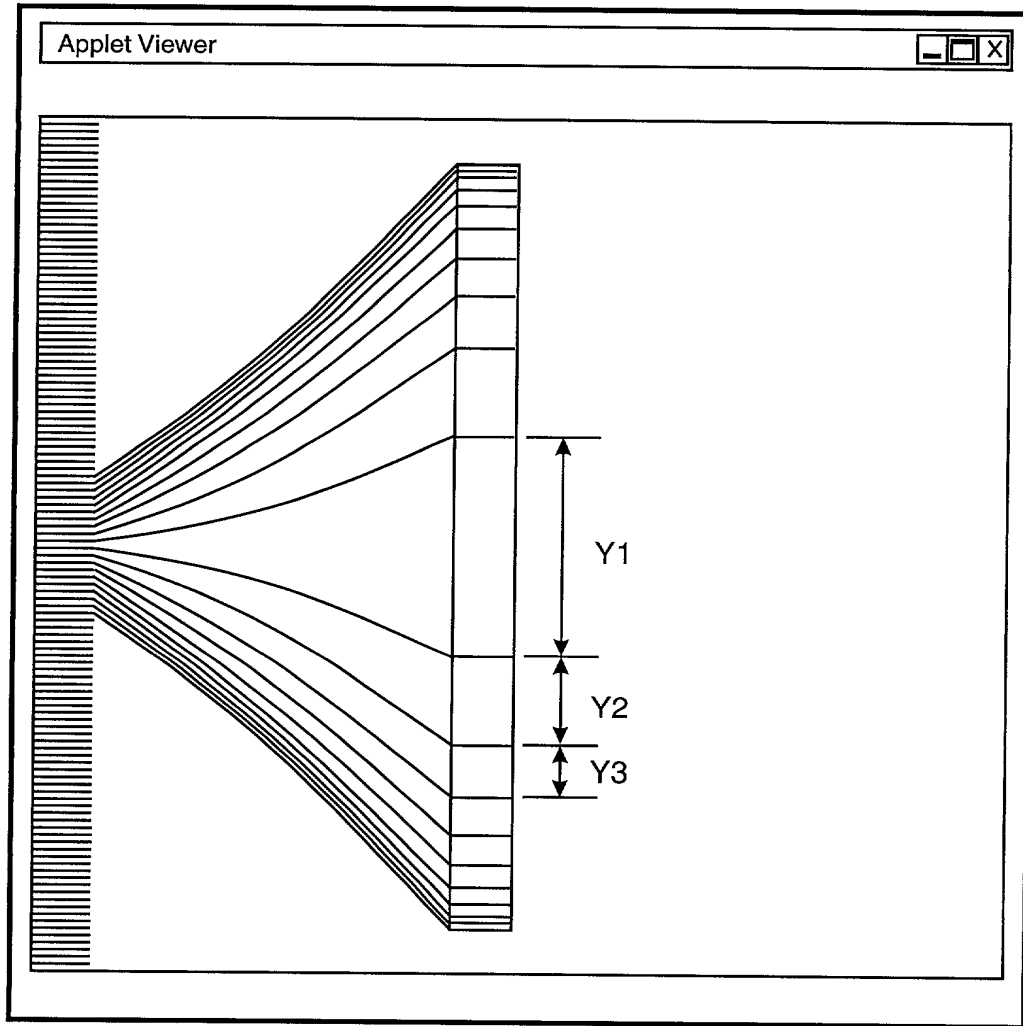


FIG. 5B



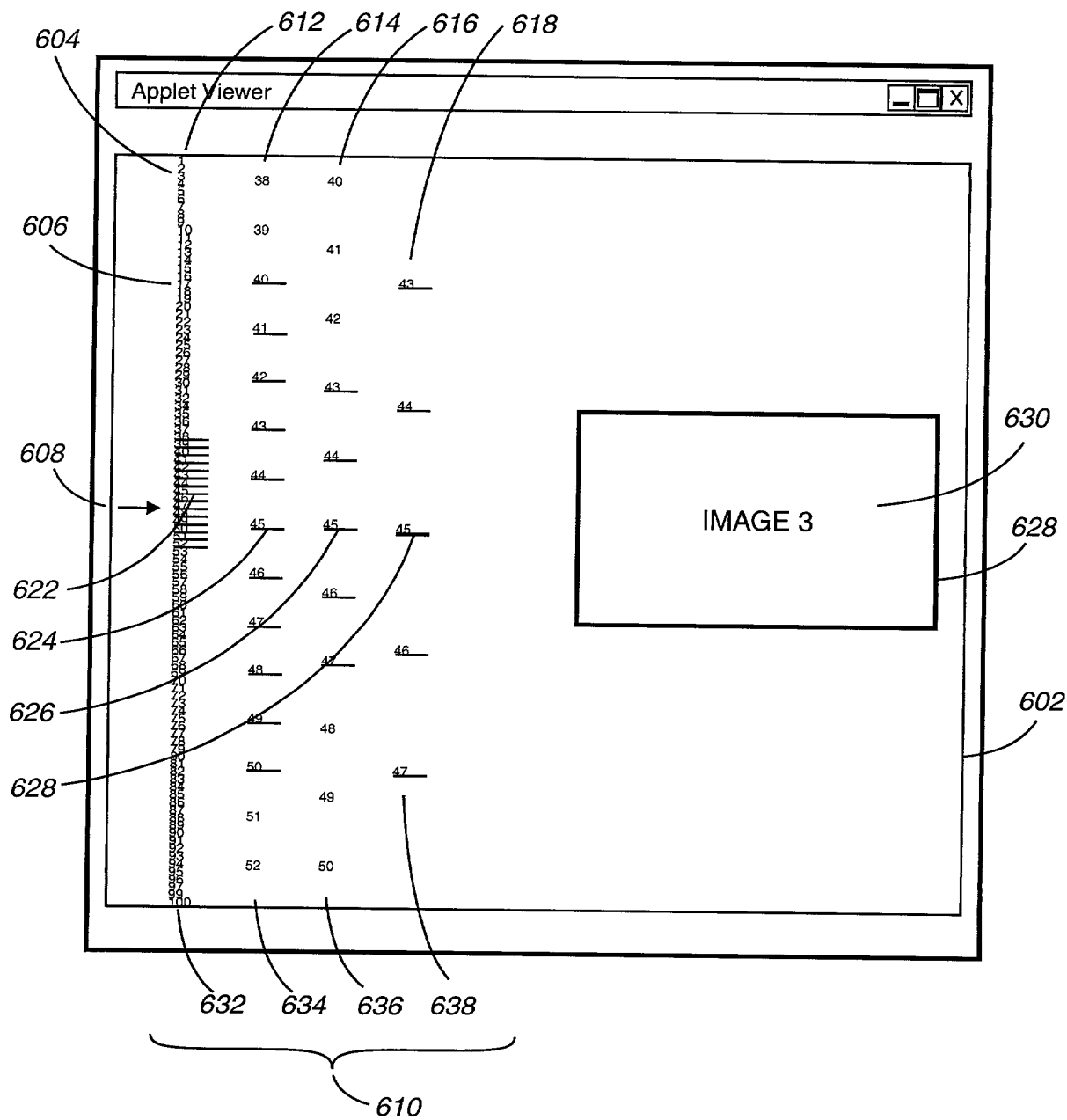
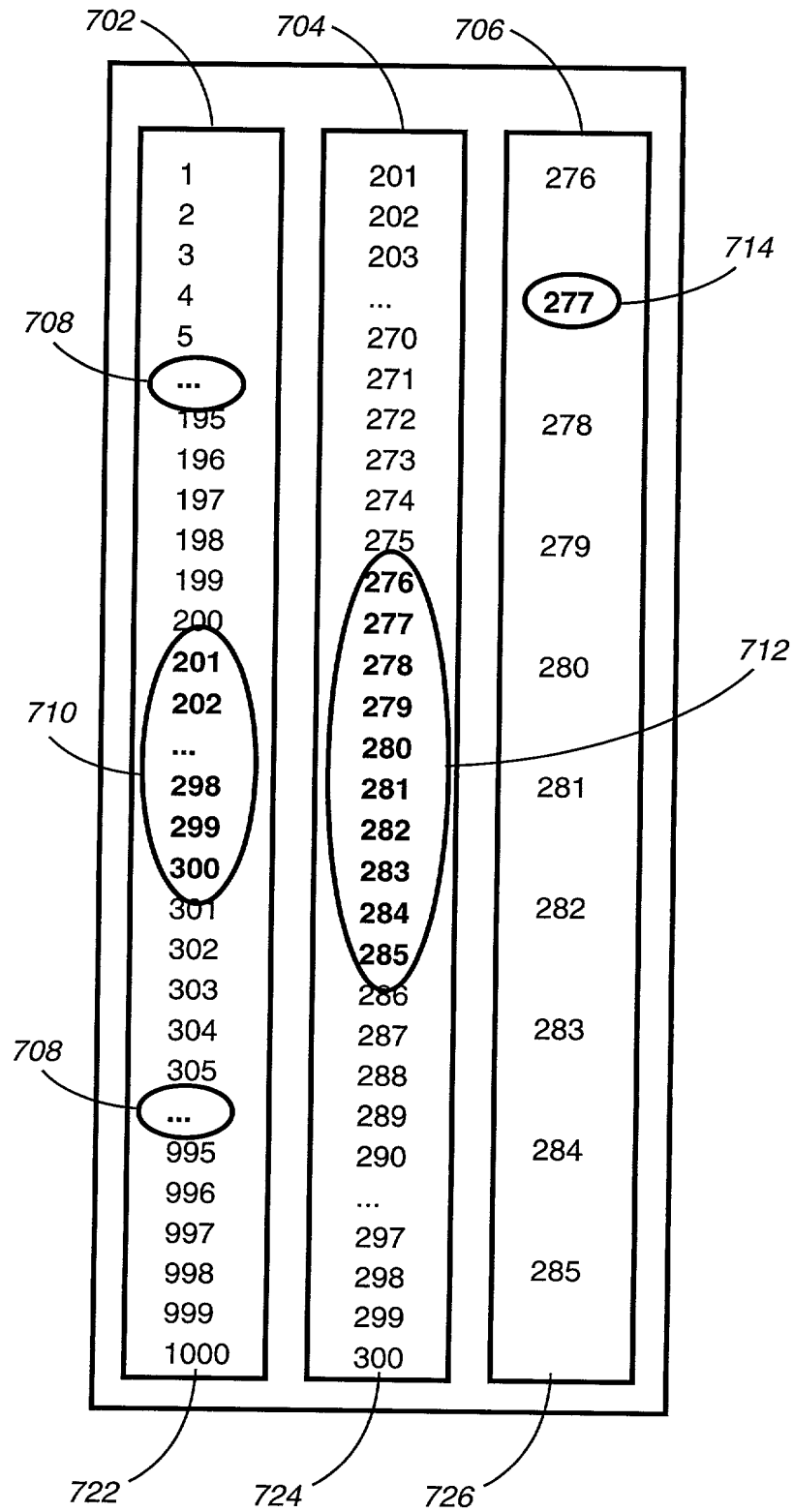


FIG. 6

600





700

FIG. 7



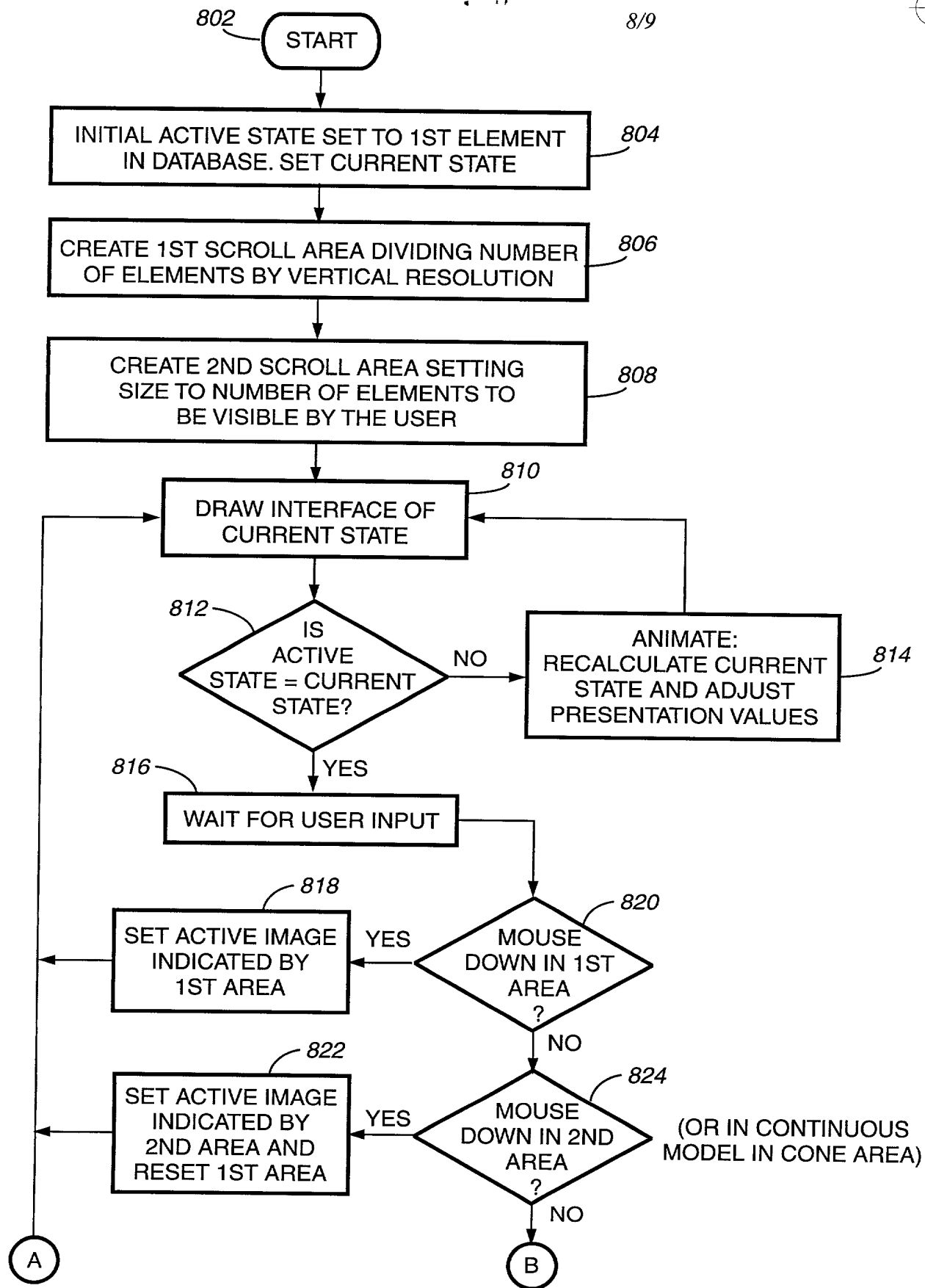
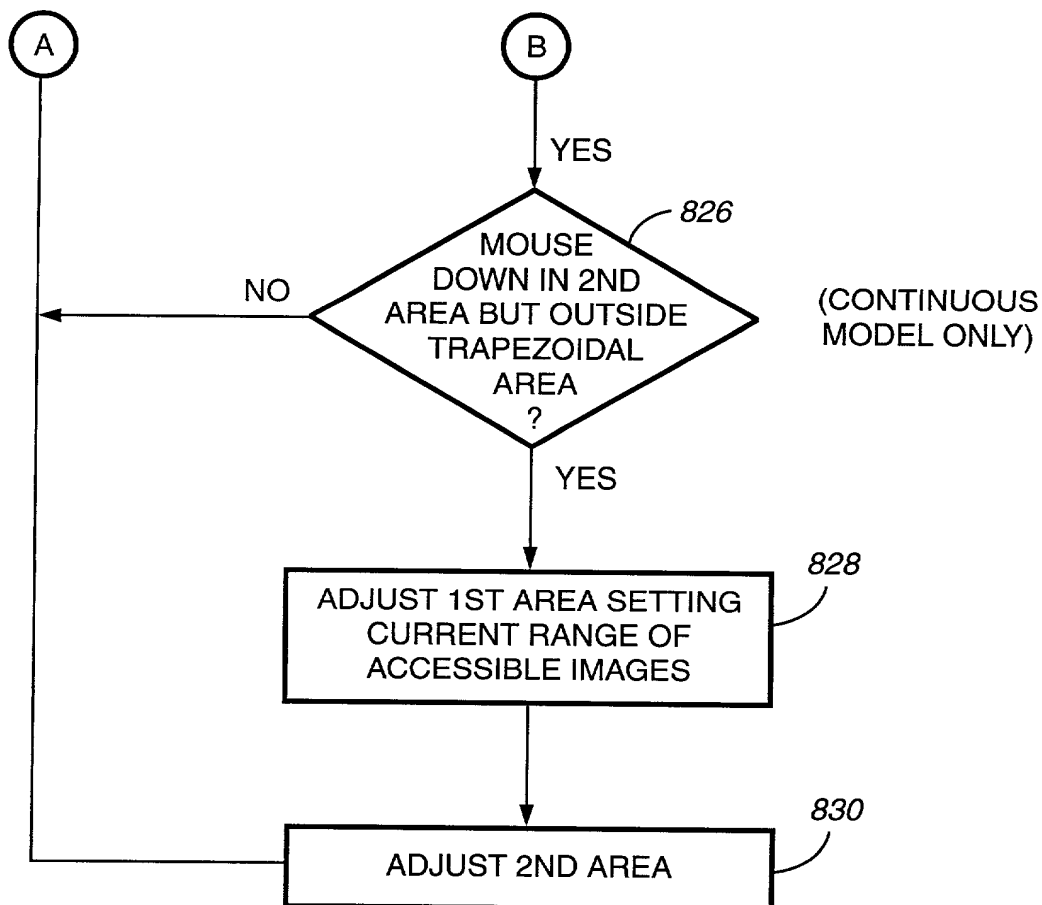


FIG. 8A

**FIG. 8B**